Using as a Feather M0, so restrictions

* Only get the GPIO’s used on the Feather
* Analog can be used as Digital on Feather
* Want to keep as much as same for debug purposes
* CAN use internal pull ups & should be available for pull downs
* Can source 7 mA

Design Points

* USB Data+ at PA25 and USB Data- at PA24
* 1 uF Cap at VddCore
* 1 uF for VDDs
* 1 uF and 0.1 uF for VDDANA
* Reset should go to pushbutton w/ 0.1 uF cap and pull-up